| Notice of Allowability | Application No. | Applicant(s) | Applicant(s) | |
|---|---|---|---------------------------|--|
| | 10/090,995 | MATSUNAGA ET AL. | | |
| | Examiner | Art Unit | | |
| | Connie C. Yoha | 2818 | | |
| The MAILING DATE of this communication appeals all claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED in the or other appropriate communities. This application is sub- | his application. If not include ication will be mailed in due | ed course. THIS | |
| 1. This communication is responsive to <u>5/7/04</u> . | | | | |
| 2. The allowed claim(s) is/are <u>1-4,8-10 and 14-32.</u> | | | | |
| 3. $igotimes$ The drawings filed on <u>07 May 2004</u> are accepted by the Ex | caminer. | | | |
| 4. Acknowledgment is made of a claim for foreign priority unally all b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" | e been received. e been received in Application cuments have been received i | No n this national stage applica | | |
| noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | itted. Note the attached EXAM | | IOTICE OF | |
| 6. CORRECTED DRAWINGS (as "replacement sheets") mus (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the sheet. The DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT | st be submitted. son's Patent Drawing Review (. s Amendment / Comment or in .84(c)) should be written on the he header according to 37 CFR sit of BIOLOGICAL MATER | PTO-948) attached the Office action of drawings in the front (not the 1.121(d). RIAL must be submitted. | | |
| Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 3/02 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material | 6. Interview Sun Paper No./M 7. Examiner's A 8. Examiner's St | rmal Patent Application (PTonmary (PTO-413), ail Date mendment/Comment tatement of Reasons for Allo | · | |

DETAILED ACTION

Response to Arguments

1. Examiner took notice of the remarks and amendments made by applicant filed on 5/7/04.

Response to Amendment

- 2. This office action is in response to Amendment filed on 5/7/04.
 - Claim 1-4, 10, and 16-19 are amended.
 - Claims 5-7, and 11-13 are canceled.
 - Claims 24-32 are newly added.
- 3. Claims 1-4, 8-10, 14-32 are pending.

Allowable Subject Matter

- 4. Claims 1-4, 8-10, 14-32 are allowed.
- 5. Claims 1-4, 8-10, 14-32 are considered allowable since prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations of a non-volatile memory device having in combination with other features, a voltage applying circuit for when a second one of the memory transistor of the NAND cell counted from the bit line side is selected in the data write mode, applying a write voltage to a control gate of the second memory transistor applying a reference voltage to a control gate of a third memory transistor, which being non-selected, counted from the bit-line side, applying a medium voltage lower than the write voltage to a control gate of a first memory transistor, which being non-selected, counted from the

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bit line side, and applying the medium voltage to at least one of the remaining memory transistors, which being non-selected.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to whose telephone number is (571) 272-1799. The examiner can normally be reached on Mon. - Fri. from 8:00 A.M. to 5:30 PM. The examiner's supervisor, David Nelms, can be reached at (571) 272-1787. The fax phone number for this Group is (703) 872-9306. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-0956.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov should you have questions on access to the Private Pair system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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C.Yoha

July 2004

Connie C. Yoha

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